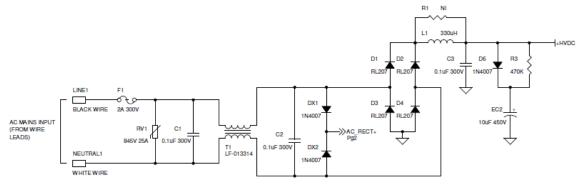
EXHIBIT 7



POWER INPUT AND CONDITIONING

